

**SAPTHAGI' COLLEGE OF ENGINEERING, ELECTRONICS ENGINEERING
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING**

TIME TABLE FOR INTERNAL ASSESSMENT TEST - I EVEN Semester (2016 - 2017)

Sl. No.	DATE	TIMINGS	8 th sem		
			4 th sem	6 th sem	8 th sem
1	20-03-2017 MONDAY	9.00am to 10.00 am 10.30am to 11.30pm 1.30pm to 2.30 pm 3.00pm to 4.00pm	15MAT41-ENGINEERING MATHAMATICS-IV 15EE42-POWER GENERATION AND ECONOMICS	10EE61-POWER SYSTEM ANALYSIS AND STABILITY 10EE62-SWITCHGEAR AND PROTECTION	10EE81-ELECTRIC DESIGN, ESTIMATING AND COSTING
2	20-03-2017 MONDAY	9.00am to 10.00 am 10.30am to 11.30pm	15EE43-TRANSMISSION AND DISTRIBUTION	10EE63-ELECTRICAL MACHINE DESIGN	10EE82-POWER SYSTEM OPERATION AND CONTROL
3	21-03-2017 Tuesday	1.30pm to 2.30 pm 3.00pm to 4.00pm	15EE44-ELECTRIC MOTORS	10EE64-DIGITAL SIGNAL PROCESSING	10EE83-RENEWABLE ENERGY SOURCES
4	21-03-2017 Tuesday	9.00am to 10.00 am 10.30am to 11.30pm	15EE45-ELECTROMAGNETIC FIELD THEORY	10EE65-COMPUTER AIDED ELECTRICAL DRAWING	10EE84-ELECTRICAL DISTRIBUTION SYSTEM
5	22-03-2017 Wednesday	1.30pm to 2.30 pm 3.00pm to 4.00pm	15EE46-OPERATIONAL AMPLIFIERS AND LINEAR ICS	10EE66/662-OPERATIONAS RESEARCH/ADVANCED POWER ELECTRONICS	
6	22-03-2017 Wednesday	4.00pm to 4.15pm	CIVIL for Lateral Entry		

TEST CO-ORDINATORS

Principal
Sapthagiri College of Engineering
Chikkasandra, Hesaleghatta Road,
Bangalore-560 057

HOD EEE DEPT.
Professor & HOD
Department of Electrical &
Electronics Engineering
Sapthagiri College of Engineering
BANGALORE - 560 057

PRINCIPAL

Sapthagiri College of Engineering
Chikkasandra, Hesaleghatta Road,
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SAPTHAGIRI COLLEGE OF ENGINEERING, BANGALORE - 57

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

TIME TABLE FOR INTERNAL ASSESSMENT TEST -II EVEN Semester (2016 - 2017)

Sl. No.	DATE	TIMINGS	4 th sem	6 th sem	8 th sem
1	24-04-2017 MONDAY	9.00am to 10.00 am	15MAT41-ENGINEERING MATHEMATICS-IV	10EE61-POWER SYSTEM ANALYSIS AND STABILITY	10EE81-ELECTRIC DESIGN, ESTIMATING AND COSTING
2	24-04-2017 MONDAY	10.30am to 11.30pm	15EE42-POWER GENERATION AND ECONOMICS	10EE661/662-OPERATIONAS RESEARCH/ADVANCED POWER ELECTRONICS	10EE82-POWER SYSTEM OPERATION AND CONTROL
3	25-04-2017 Tuesday	9.00am to 10.00 am	15EE43-TRANSMISSION AND DISTRIBUTION	10EE63-ELECTRICAL MACHINE DESIGN	
4	25-04-2017 Tuesday	10.30am to 11.30pm	15EE44-ELECTRIC MOTORS	10EE64-DIGITAL SIGNAL PROCESSING	10EE836-RENEWABLE ENERGY SOURCES
5	26-04-2017 Wednesday	1.30pm to 2.30 pm	15EE45-ELECTROMAGNETIC FIELD THEORY	10EE65-COMPUTER AIDED ELECTRICAL DRAWING	10EE844-ELECTRICAL DISTRIBUTION SYSTEM
6	26-04-2017 Wednesday	3.00pm to 4.00pm	15EE46-OPERATIONAL AMPLIFIERS AND LINEAR ICs	10EE62-SWITCHGEAR AND PROTECTION	
		4.00pm to 4.15pm	CPH for Lateral Entry		

TEST CO-ORDINATORS

PHODS-DEELED
Department of Electrical &
Electronics Engineering
Sapthagiri College of Engineering

Principal
Sapthagiri College of Engineering
Chittabasandra, Hesaraghatta Road,
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SAPTHAGIRI COLLEGE OF ENGINEERING, BANGALORE - 57
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
INTERNAL ASSESSMENT TEST -III EVEN Semester (2016 -

TIME TABLE FOR INTERNAL ASSESSMENT TEST - III		8th sem

Sl. No.	DATE	TIMINGS	4 th sem	6 th sem	8 th sem
1	24-05-2017 WEDNESDAY	9.00am to 10.00 am 10.30am to 11.30pm 1.30pm to 2.30 pm 3.00pm to 4.00pm	15MAT41-ENGINEERING MATHEMATICS-IV 15EE42-POWER GENERATION AND ECONOMICS	10EE61-POWER SYSTEM ANALYSIS AND STABILITY 10EE661/662-OPERATIONAS RESEARCH/ADVANCED POWER ELECTRONICS	10EE81-ELECTRIC DESIGN, ESTIMATING AND COSTING 10EE82-POWER SYSTEM OPERATION AND CONTROL 10EE836-RENEWABLE ENERGY SOURCES
3	25-05-2017 THURSDAY	9.00am to 10.00 am 10.30am to 11.30pm	15EE43-TRANSMISSION AND DISTRIBUTION	10EE63-ELECTRICAL MACHINE DESIGN	10EE844-ELECTRICAL DISTRIBUTION SYSTEM
4	25-05-2017 THURSDAY	1.30pm to 2.30 pm 3.00pm to 4.00pm	15EE44-ELECTRIC MOTORS	10EE64-DIGITAL SIGNAL PROCESSING	
5	26-05-2017 FRIDAY	9.00am to 10.00 am 10.30am to 11.30pm	15EE45-ELECTROMAGNETIC FIELD THEORY	10EE65-COMPUTER AIDED ELECTRICAL DRAWING/	
6	26-05-2017 FRIDAY	1.30pm to 2.30 pm 3.00pm to 4.00pm 4.00pm to 4.15pm	15EE46-OPERATIONAL AMPLIFIERS AND LINEAR ICs CPH for Lateral Entry	10EE62-SWITCHGEAR AND PROTECTION	


Principal
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WEST CO-ORDINATORS

450
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Electronics Engineering
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SAPTHAGIRI COLLEGE OF ENGINEERING

THIRD "IA-TEST" TIME TABLE

SEMESTER: I		BRANCH: CSE, ISE, EEE, ECE, ME, BT & CIVIL	
DATE	TIME	P-CYCLE	C-CYCLE
14/11/2016 (Monday)	9:00AM to 10:00AM	Engineering Mathematics-I 15MAT11	
	1:30PM to 2:00PM	Constitution of India & Professional ethics 15CPH18	Environmental Studies 15CIV18
15/11/2016 (Tuesday)	9:00AM to 10:00AM	Engineering Physics 15PHY12	Engineering Chemistry 15CHE12
	1:30PM to 2:30PM	Elements of Civil Engineering 15CIV13	C-programming & Data structure 15PCD13
16/11/2016 (Wednesday)	9:00AM to 10:00AM	Elements of mechanical Engineering 15EME14	Basic Electronics 15ELN15
	1:30PM to 2:30PM	Basic Electrical Engineering 15ELE15	-----

NOTE: Students are informed to contact respective department test coordinator for seating arrangement and other details related to IA test.

Instructions to the Students

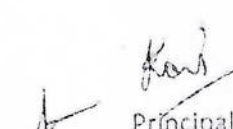
1. Students with valid COLLEGE ID CARD are allowed to write IA Test.
2. Uses of mobile phones, IPODS or any other storage devices are strictly prohibited during the Test.
3. Students are informed to report at least 10 minutes before the commencement of the Test.
4. Students are informed to write all exams in BLACK BALL POINT PEN only.
5. Leave all unauthorized aids in your bag/ (or at home).


Coordinator
P-cycle

Dr. Jagadeesha Gowda, G.V
Msc, BEd, M Phil, PhD.
Professor & Head
Department of Physics
Sapthagiri College of Engineering
Bengaluru-560 057.


Coordinator
C-cycle

Dr. B.S. Krishna
Professor & HOD
Department of Chemistry
Sapthagiri College of Engineering
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Principal
SCE


Principal
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