SAPTHAGIRI COLLEGE OF ENGINEERING

#14/5, Chikkasandra, Hesaraghatta Main Road, Banglore-560057, Karnataka, India
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

FACULTY PROFILE

Name of the faculty	**P Agalya**
Designation	Associate Professor
Contact Number	9448509719
Email ID	agalyas@sapthagiri.edu.in

Educational Qualification

Degree	Specialization	University	Year of Passing
M.E	Applied Electronics	Bharathiar University	1999
B.E	Electronics & Communication Engg.	Bharathiar University	1997

Work Experience

Teaching	Research	Industry
19	3	nil

Publications

National/International Journals	National/International Conferences	Books Authored/Edited
2		nil

Area of Interest

Digital Electronics	**Microprocessors**
Microcontrollers	Embedded System Design
Image Processing	VLSI

Membership in Professional Bodies/University Bodies/Organization

Nil

Funded Project	
Patent	

Publications National/International Journals

1. Chandana P, AgalyaP,"Energy Efficient Decoder Architecture for Communication Systems" International Journal of Science, Engineering and Technology Research (IJSETR), Volume 3, Issue 4, April 2014, ISSN: 2278-7798.
2. Megha S. Hegde, Agalya p, “Development and implementation of VLSI configurable architecture for Gabor filter in medical imaging application”, International journal of engineering and management research, June-2018, Volume -8 , Issue -3, ISSN 2250-0758

Publications

National /International Conferences

1. Agalya P, Hanumantharaju M.C., Gopalakrishna,“A detailed review on color image contrast enhancement algorithms for real time applications,” Third International conference on Information system design and intelligent applications.
2. Varshini K., Agalya P., B.N. Shobha, “Implementation of an Efficient Enhancement Algorithm for Images in Consumer Electronic Devices2nd National Conference on Emerging Trends in Science & Technology, Sapthagiri College of Engineering, Bangalore, 12th May 2015

3. Mallikarjun S.H., Agalya P. “Low Power SRAM Implementation Using Asymmetric 10T System in 90nm Technology,” National Conference on Electrical & Electronics Engineering (NCEEE-2014), HKBK College of Engineering, Bangalore, 5th June 2014.

4. Chandana P, Agalya P, B.N. Shobha, “Design and FPGA Implementation of Energy Efficient Decoder Architecture for Communication System”, National Conference on VLSI Signal Processing, Communication and Soft Computing-2014 (NCVCS-14), 8/05/2014, Sri Krishna Institute of Technology, Bangalore.
5. Chaitra P., Agalya , ““Hardware Implementation of Image Enhancement algorithm in FPGA using ANN & Fuzzy logic”, National Conference on recent trends in Electronics and communication Engineering at SJCIT,Chikkabalapura.

Faculty Development Programme Participated

- 2 days FDP on Machine Learning and Tools held on 30th& 31st January 2019 at SCE
- Preconference Expert lectureseries held on 18th July 2018 as a part of 1st ICPSPCT at Ramaiah University.
- 2 Days FDP on Research Proposal preparation towards PhD Program on 24th& 25th June 2016 at KSSEM, Bengaluru.
- 5 Days FDP on ARM CORTEX M3 &Embedded Controller held on 1st – 5th Jan 2018 at BIT, Bengaluru.
- 2 Days FDP on NuvotonNuMicro ARM Cortex M0 and its Applications under TEQIP-II on 10th& 11th September 2013 held at MSRIT, Bengaluru.

Workshop Attended

1. 5 Days Research Promotion Workshop from 24th to 28th July 2017 at BMSIT, Bengaluru.
2. 5 Days Workshop on Avnced Embedded System Design from 4th July – 8th July 2012at VIT University, Vellore

Awards/Recognition/Achievement/Others

- Resource person for a two days Workshop in EEE department on “Digital Signal Processing Laboratory” from 22ndto 23rdJanuary 2018. Topics covered includes Introduction to Matlab, All the lab Experiments with hands -on experience.
- Organised, Conducted & delivered the course content for a 5 day Bridge course on “Introduction to ARM Microcontroller and Embedded system” in ECE Deapartment
- Session chairfor:

2nd National Conference on Emerging Trends in Science & Technology, Sapthagiri College of Engineering, Bangalore, 12th May 2015