SAPTHAGIRI COLLEGE OF ENGINEERING

#14/5, Chikkasandra, Hesaraghatta Main Road, Banglore-560057, Karnataka, India

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

FACULTY PROFILE

Name of the faculty	**Varshini K**
Designation	Assistant Professor
Contact Number	9164379191
Email ID	varshinik@sapthagiri.edu.in

Educational Qualification

Degree	Specialization	University	Year of Passing
M. Tech.	VLSI Design & Embedded Systems	VTU	2015

Work Experience

Teaching	Research	Industry
5	-	-

Publications

National/International Journals	National/International Conferences	Books Authored/Edited
1		-

Area of Interest

VLSI Design
Embedded Systems

Subjects Taught

Control Systems	Synthesis & Optimization of Digital Circuits	Digital System Design Lab
Digital System Design	Real Time Operating Systems	LIC & Communication Lab
Electronic Circuits	Embedded System Design	Analog Electronics Lab
Analog & Mixed mode VLSI Design	Management & Entrepreneurship Development	
Advances in VLSI Design	VLSI Lab	

In House Experience

Sl. No.	Designation	Period	
		From	To
1	Assistant Professor	20-07-2015	Till Date

Publications

National/International Journals

- Efficient Brightness Preserving Algorithm for Images in Consumer Electronic Devices

National /International Conferences

- Implementation of an Efficient Enhancement Algorithm for Images in Consumer Electronic Devices

Faculty Development Programme Participated

- FDP on Physical Design and its Verification held at Acharya Institute of Technology
- FDP on Paper Writing using Latex held at Sapthagiri College of Engineering
- FDP on Outcome Based Education held at Sapthagiri College of Engineering