



SAPTHAGIRI COLLEGE OF ENGINEERING

#14/5, Chikkasandra, Hesaraghatta Main Road, Bangalore-560057, Karnataka, India

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

FACULTY PROFILE

Name of the faculty	Sudha M S
Designation	Assistant Professor
Contact Number	9480616926
Email ID	sudhams@sapthagiri.edu.in



Educational Qualification

Degree	Specialization	University	Year of Passing
BE	Electronics and communication	Bangalore University	1993
M Tech	Applied Electronics	MGR Deemed university	2007
(Ph.D)	Multimedia Security	Jain Deemed to be University	Waiting for Viva

Work Experience

Teaching	Research	Industry
18	-	2

Publications

National/International Journals	National/International Conferences	Books Authored/Edited
International Journals -5	International Conferences-2	-

Area of Interest

Multimedia security	Verilog programming for digital circuits
Networking and Network security	VLSI Design
Image processing	NS2
Digital signal processor and programming	Programming using VHDL

Subject Taught

Basic Electronics	Signals and system	Microprocessor(8086)
Fundamental of HDL	Network analysis	Linear integrated circuit
DSP A &A	Electronics circuits	Digital signal processing
Network security	Electronic Devices	Digital electronics

Outside Experience

Sl. No	Place of Work	Designation	Period	
			From	To
1	HMS polytechnic	Lecturer and HOD	1992	1994
2	Telics India Limited Trivendrum,Kerala	Quality control incharge and instructor	1994	1996
3	MultiData institute for AMIE, IETE, DIPIETE	Lecturer	2000	2005
4	Sarabhai institute of Science and Technology (Cochin university)	Assistant professor	2007	2008(March)

Publications

- 1.Sudha, M. S., and T. C. Thanuja. "A Robust Image Watermarking Technique using DTCWT and PCA." International Journal of Applied Engineering Research 12.19 (2017): 8252-8256.
2. Sudha, M. S., and T. C. Thanuja. "Effect of different attacks on image watermarking using dual tree complex wavelet transform (DTCWT) and principle component analysis (PCA)." International Journal of Engineering & Technology 7.2 (2018): 1-5.
3. Sudha M S and T. C. Thanuja. "FPGA Implementation of DTCWT and PCA Based Watermarking Technique." International Journal of New Innovations in Engineering and Technology Volume 10 Issue 1 February 2019 ISSN: 2319-6319.
4. K. Deepika, Sudha M S, Sandhya Rani M H "FPGA Implementation of Digital Watermarking Using Integer Wavelet Transform and AES Techniques" International Journal of Engineering and Techniques - Volume 2 Issue 2, ISSN: 2395-1303 Mar – Apr 2016.5.Tejaswini S sudha M S Shobha

International conference

B N "Design and FPGA implementation of reliable SHA-3algorithm" International Journal of Science and Technology ISSN2321-919xvolume3,Issue 5,2015

, 1. Sudha M S ,Dr. Thanuja.T C "DIGITAL IMAGE AUTHENTICATION (DIA)-A SURVEY." IRF International Conference, Bangalore 23rd March-2014, ISBN: 978-93-82702-68-9.

2. Sudha, M. S., and T. C. Thanuja. "Randomly tampered image detection and self-recovery for a text document using Shamir secret sharing." IEEE International Conference on Recent Trends in Electronics, Information & Communication Technology (RTEICT). IEEE, 2016.

Workshop Attended

1-Days workshop on" Intensive Training in practical counselling Skills "	SCE Bangalore	2019-20
5-Days workshop on" Physical Design and its verification "	Acharya institute of Technology Bangalore	22 nd to 26 th July 2019
3-days workshop on computer network lab	Don Bosco institute of technology	8 to 10 January 2018
1-day workshop on Cadence Software Installation and trouble shooting	Entuple Technologies Pvt Limited	26 th June 2015
3-days national workshop on "signal and image processing with applications"	RVCE Bangalore	1 to 3 January 2013
1-Day Hands-on work shop on Sci Lab	UTL Technologies	October 20 th 2012
5-days Faculty Empowerment work shop	Mission10x	30 th Jan to 3 rd Feb 2012
Two-days on VLSI Lab work shop	DSCE, Bangalore campus	20 and 21 st August 2009

Faculty Development Programme Participated

3-days FDP on teacher empowerment	sce	23 to 25 July 2018
1-day FDP	sce	22 nd January 2015
1-day FDP on VLSI Lab	SJBIT B Bangalore	3 rd July 2009